

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3839	error same (detection or correction) same memory near4 device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 12:34
L2	2991	1 and control\$4 same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 12:35
L3	59	2 and (inverter and encoder and decoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:05
L4	42	3 and (ECC or error near4 correction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 12:36
L5	26	4 and (memory).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 12:44
L6	25	5 and invert\$4 same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 12:44
L7	20	5 and invert\$4 same bit same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:12
L8	3115	memory adj control\$4 near6 memory adj device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:04
L9	23	8 and (inverter and encoder and decoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:05

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L10	3	9 and (ECC or error adj correction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:06
L11	71640	(control\$4 near4 circuit\$4 or memory near4 control\$4 or control\$4) near5 (memory near4 device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:14
L12	7544	((control\$4 near4 circuit\$4 or memory near4 control\$4 or control\$4) near5 (memory near4 device)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:14
L13	355	12 and (inverter and decoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:17
L14	0	13 and encoder same (error near4 correction or ECC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:16
L15	1	13 and (encoder and (error near4 correction or ECC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:16
L16	1	12 and (inverter and decoder and encoder and error adj correction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 13:18

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14757	(control\$4 circuit\$4 memory).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:51
L2	759	1 and inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:52
L3	280	2 and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:52
L4	164	3 and control\$4 same circuit\$4 same memory adj device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:31
L5	1	4 and (bit data read\$4 memory device decoder error correction decod\$4 output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:33
L6	164	4 and (bit data read\$4 memory device decoder error correction decod\$4 output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 10:33
L7	1	4 and (bit near4 data and read\$4 near4 memory same device and decoder and error same correction and decod\$4 and output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 10:34
L8	2	4 and (decoder and error same correction and decod\$4 and output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 10:49
L9	3	4 and (decoder and error same correction or ECC) and (decod\$4 and output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/22 10:51

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L10	76924	(control\$4 memory).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:15
L11	2075	10 and inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:52
L12	862	11 and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:52
L13	47	12 and encoder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 10:52
L14	12	13 and (error near4 correction or ECC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:14
L15	452313	(control\$4 memory).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:15
L16	689	15 and (inverter and decoder and encoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:15
L17	73	16 and (ECC or error adj correction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:16
L18	0	17 and invert\$4 same dib same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:16
L19	32	17 and invert\$4 same bit same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:16

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L20	15	19 and decoder same (error or ECC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 11:40
L21	5	20 and nonvolatile	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 12:00
L22	5391	inverter same invert\$4 same bit same data same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 12:02
L23	4	22 and decoder same execut\$4 same error near4 correction and decod\$4 same inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/07/22 12:03